



1204.46258X00

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. TASAKI, et al.  
Application No.: 10/581,721  
Nat'l Stage Entry: JUNE 5, 2006  
For: MANUFACTURING METHOD FOR ELECTRONIC DEVICE  
Group: TBA  
Examiner: TBA  
Confirm. No.: 8570

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR 1.97 AND 1.98**

**Mail Stop: AMEND – NO FEE**

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

September 5, 2006

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find copies of documents cited in the specification of the above-identified application, for example, in the paragraph bridging pages 1 and 2, and in the first full paragraph on page 3, thereof. Also enclosed herewith is a copy of an English-language abstract of the cited Japanese Patent Document No. 2002-269520, and of the publication by Susumu, et al. Further enclosed is a Form PTO/SB/08A, listing the enclosed documents.

This Information Disclosure Statement is being submitted prior to a first Office Action on the merits in the above-identified application, and within three (3) months of the date of entry thereof in the National Stage. Clearly, requirements of 37 CFR 1.97(b) are satisfied.

To the extent that the enclosed documents are not in English, it is respectfully submitted that requirements of 37 CFR 1.98(a)(3) are satisfied by the English-

September 5, 2006


language abstracts submitted respectively with the non-English language documents, as referred to previously, and/or by discussion of the enclosed documents in the specification of the above-identified application, e.g., in the paragraph bridging pages 1 and 2, and in the full paragraph on page 3, of the specification of the above-identified application.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR 1.97 and 1.98 have been satisfied, in connection with each of the documents listed on the enclosed Form. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

Please charge any shortage of fees due in connection with the filing of this paper to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (case No. 1204.46258X00), or credit any excess fees paid to such Deposit Account.

Respectfully submitted,

**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

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TO/SB/08A (08-03)  
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<b>Substitute for form 1449A/PTO</b> <b>INFORMATION DISCLOSURE STATEMENT</b> <b>BY APPLICANT</b>  <b>September 5, 2006</b>  <small>(Use as many sheets as necessary)</small>		<b>Complete if Known</b>	
		Application Number	10/581.721
		Filing Date	June 5, 2006
		First Named Inventor	K. TASAKI et al.
		Art Unit	TBA
		Examiner Name	TBA
		Attorney Docket Number	1204.46258X00

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
		US-				
		US-				
		US-				
		US-				
		US-				
		US-				

FOREIGN PATENT DOCUMENTS						
Examiner Initials'	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> –Number <sup>4</sup> –Kind Code <sup>5</sup> (if known)				
		JP 2002-269520	09/20/2002	M. Usami		A

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>*</sup>	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		K. Susumu, et al., "VLSI Packaging Technology", First and Second Volumes, Nikkei BP, 1993, pages 1-5, 9-13, 78, 79 and 104-108.	A
		M. Usami, et al., "Powder LSI: An Ultra Small RF Identification Chip for Individual Recognition Applications", 2003 IEEE International Solid-State Circuits Conference, Session 22, TD: Embedded Technologies, Paper 22.7, pp. 398, 399 and 501.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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